WEST Search History

| Hide Items | Restore | Clear | Close |

DATE: Monday, October 11, 2010

Hide?	<u>Set Name</u>	<u>Query</u>	**Hit Count**
Interference			
		DB=PGPB,USPT,UPAD; PLUR=YES; OP=ADJ	
☐	L20	L18 and (virtual blocks).clm.	0
☐	L19	L18 and (virtual block).clm.	0
☐	L18	L17 and L15	4
☐	L17	L14 and (graphical user interface).clm.	86
☐	L16	L14 and (user module).clm.	14
☐	L15	L14 and (user module).clm.	14
☐	L14	(microcontroller).clm.	12680
Prior Art			
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L13	L9 and L3	6
		DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L12	L6	20
☐	L11	L5	524
☐	L10	L9	0
		DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L9	L8 and programmable	1042
☐	L8	L7 and assembly	1427
☐	L7	L6 and counter	3219
☐	L6	L5 and (analog-to-digital or ADC)	5861
☐	L5	L4 and timer	25697
☐	L4	micro?controller or microcontroller	150414
☐	L3	(717/106 l717/107 l717/108 l717/109 l717/110 l717/111 l717/112 l 717/113).ccls.	3556

| ☐ | L2 | 09/998,848 | 9 |
| ☐ | L1 | 09/998848 | 9 |

END OF SEARCH HISTORY